

PATENT NUMBER

U.S. UTILITY Patent Application

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SCANNED	LA a.A. Mc	

APPLICATION NO. 09/531026	CONT/PRIOR D	CLASS 703	SUBCLASS 14	ART UNIT 2763	EXAMINER BRADA DAY
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APPLICANTS

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Time

System and method for validating processor performance and functionality

PTO-2040
12/99

ISSUING CLASSIFICATION															
ORIGINAL					CROSS REFERENCE(S)										
CLASS			SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)								
INTERNATIONAL CLASSIFICATION															

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ _____ (Primary Examiner) (Date)		ISSUE FEE	
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